Performance Analysis of Computer Networks (Winter, 2005)

CRN: 20252  
Course Number: ECE-C632  
Section Number: 501  
Credits: 3.0  
Time: Thursdays 6pm - 8:50pm  
Room: TBA  
Instructor: Steven Weber  
Restrictions: ECE-C631 w/ minimum grade of C  
Department: Electrical and Computer Engineering

Description
Covers probability theory and its applications to networks, random variable and random processes; Markov chains, multi-dimensional Markov chains; M/M/1, M/M/m, M/M/m/m, M/G/1 and G/G/1 queueing systems and their applications in computer networks; analysis of networks of queues: Kleinrock Independence Approximation; Time-reversibility and Burke’s theorem; Jackson’s theorem; the phenomenon of long-range dependence and its implications in network design and traffic engineering.

Textbook
Primary text (required)

Title: Communication Networking : An Analytical Approach  
Authors: Anurag Kumar, D. Janjunath, and Joy Kuri  
Publisher: Morgan Kaufmann  
ISBN: 0124287514  
Edition: 1st

Grading

Homework (one problem set per week) 30%  
Midterm Exam (comprehensive) 30%  
Final Exam (comprehensive) 40%

Homework and Makeup Exams
Makeup exams are only available if you are unable to attend due to a severe health problem or a death in your family. Homeworks are due at the beginning of class, one week following the class in which they were assigned. Late homeworks will not be accepted.

Students with Disabilities
In accordance with Drexel University policy, any student with a documented disability who needs accommodations is encouraged to contact the Office of Disability Services (215-895-1401) or speak directly to the professor for further information about this office. Students must register with the Office of Disability Services and receive an Accommodation Verification Form prior to receiving accommodations. Contact with the Office of Disability Services is strictly confidential. Please make contact as early in the term as possible in order to receive timely accommodations.

Mandatory Registration

www.ece.drexel.edu/courses/ECE-C632-501/  
January 5, 2005
All students sitting in the classroom during the class must be registered for the course and on the class list supplied to the instructor for the second class. Any student not on the list at that time will be asked to leave until proper registration is obtained.

**Academic Dishonesty**
The Drexel University policy on academic dishonesty may be found at http://www.drexel.edu/studentlife/studenthandbook2002/judicial/acadhon.html and will be strictly enforced. Plagiarism, fabrication, and cheating will, at the discretion of the instructor, constitute grounds for failure of the course.

**Course Calendar**
Please read the assigned materials for the lecture before the class in which it is covered.

<table>
<thead>
<tr>
<th>Class</th>
<th>Date</th>
<th>Material</th>
<th>Homework</th>
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</thead>
<tbody>
<tr>
<td>1</td>
<td>1/6</td>
<td>★ Chapter 4: <em>Stream sessions: deterministic network analysis</em>&lt;br&gt;Sections §4.1 – §4.2</td>
<td>★ HW1 assigned</td>
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<tr>
<td>2</td>
<td>1/13</td>
<td>★ Chapter 4: <em>Stream sessions: deterministic network analysis</em>&lt;br&gt;Sections §4.3 – §4.6</td>
<td>★ HW1 due&lt;br&gt;★ HW2 assigned</td>
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<td>3</td>
<td>1/20</td>
<td>★ Chapter 5: <em>Stream sessions: stochastic network analysis</em>&lt;br&gt;Sections §5.1 – §5.5</td>
<td>★ HW2 due&lt;br&gt;★ HW3 assigned</td>
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<tr>
<td>4</td>
<td>1/27</td>
<td>★ Chapter 5: <em>Stream sessions: stochastic network analysis</em>&lt;br&gt;Section §5.6</td>
<td>★ HW3 due</td>
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<td>5</td>
<td>2/3</td>
<td>Midterm Exam</td>
<td>★ HW4 assigned</td>
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<tr>
<td>6</td>
<td>2/10</td>
<td>★ Chapter 5: <em>Stream sessions: stochastic network analysis</em>&lt;br&gt;Sections §5.7 – §5.9</td>
<td>★ HW4 due&lt;br&gt;★ HW5 assigned</td>
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<tr>
<td>7</td>
<td>2/17</td>
<td>★ Chapter 5: <em>Stream sessions: stochastic network analysis</em>&lt;br&gt;Sections §5.10 – §5.11</td>
<td>★ HW5 due&lt;br&gt;★ HW6 assigned</td>
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<td>8</td>
<td>2/24</td>
<td>★ Chapter 6: <em>Circuit multiplexed networks</em>&lt;br&gt;Sections §6.1 – §6.3</td>
<td>★ HW6 due&lt;br&gt;★ HW7 assigned</td>
</tr>
<tr>
<td>9</td>
<td>3/3</td>
<td>★ Chapter 6: <em>Circuit multiplexed networks</em>&lt;br&gt;Sections §6.4 – §6.7</td>
<td>★ HW7 due&lt;br&gt;★ HW8 assigned</td>
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<tr>
<td>10</td>
<td>3/10</td>
<td>★ Chapter 6: <em>Circuit multiplexed networks</em>&lt;br&gt;Sections §6.8 – §6.9</td>
<td>★ HW8 due</td>
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<td>11</td>
<td>3/17</td>
<td>Final Exam</td>
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